

**DISTRIBUTED BUFFER INTEGRATED CACHE MEMORY  
ORGANIZATION AND METHOD OFREDUCING ENERGY  
CONSUMPTION**

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**ABSTRACT OF THE INVENTION**

A cache memory including a content addressable memory, random access memory (CAMRAM) cache and method for managing a cache to reduce cache energy consumption. A cache buffer receives incoming data and buffers a storage array. The cache buffer holds a number of most recently accessed data blocks. In any access, cache buffer locations are checked before checking the storage array.

**Y0R920030249US1**